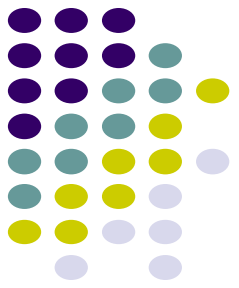
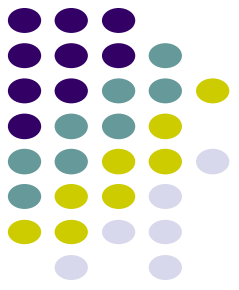


COMMON EMITTER WITH BYPASS CAPACITOR C_E

TYPE 3: With Emitter Bypass Capacitor, C_E



- **Circuit with Emitter Bypass Capacitor**
 - **There may be times when the emitter resistor must be large for the purpose of DC design, but degrades the small-signal gain too severely.**
 - An **emitter bypass capacitor** can be used to effectively create a short circuit path during AC analysis hence avoiding the effect R_E



TYPE 2: Emitter terminal connected with R_E – normally $r_o = \infty$ in this type

New parameter: input resistance seen from the base, $R_{ib} = v_b / i_b$

$$\begin{aligned}\beta &= 120 \\ V_{BE} &= 0.7\text{V} \\ V_A &= \infty\end{aligned}$$

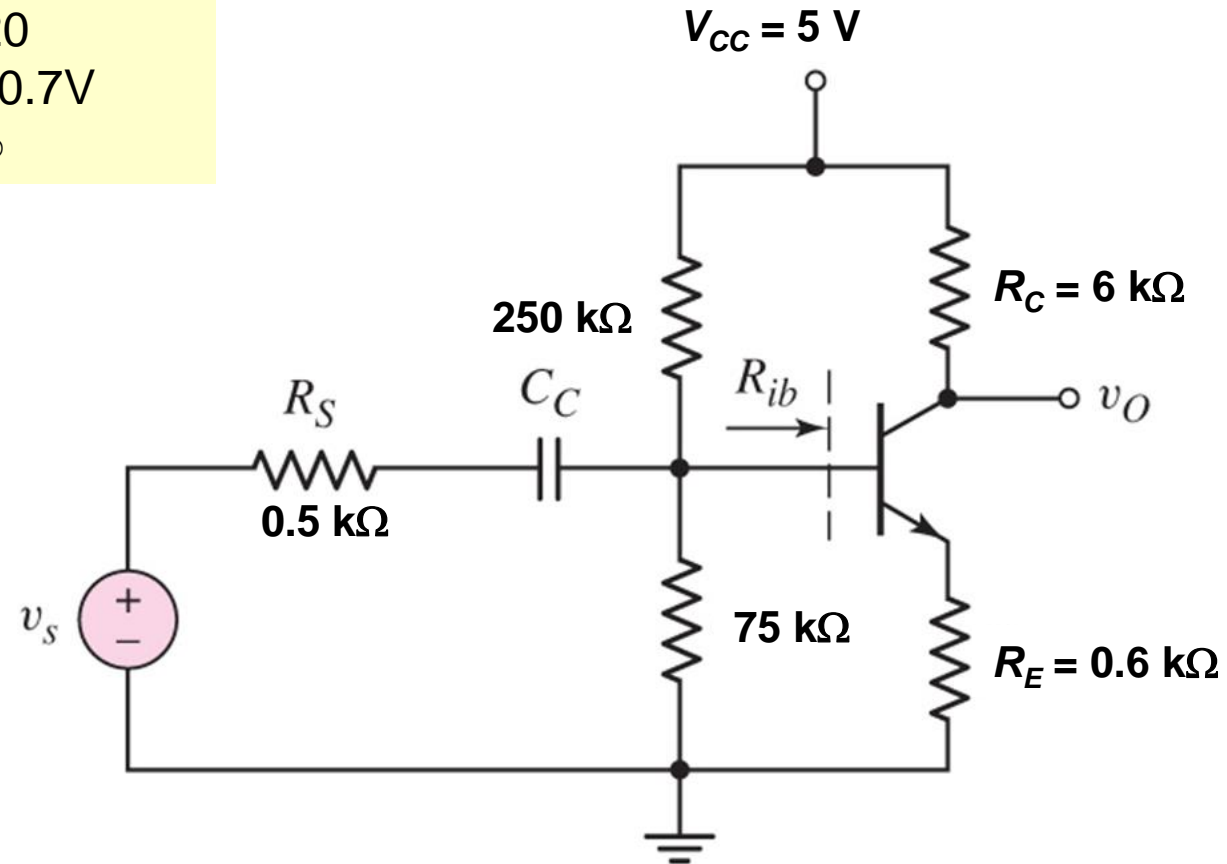
Voltage Divider biasing:

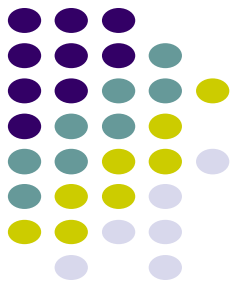
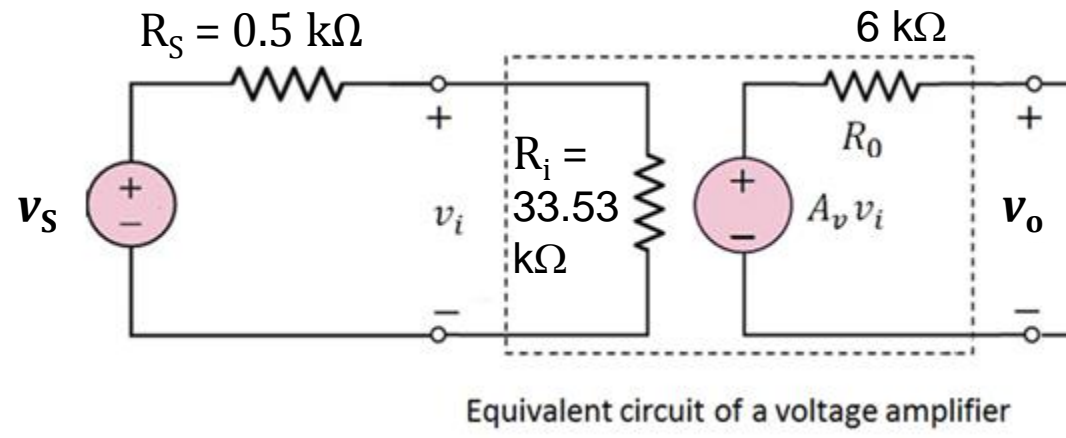
Change to Thevenin

Equivalent

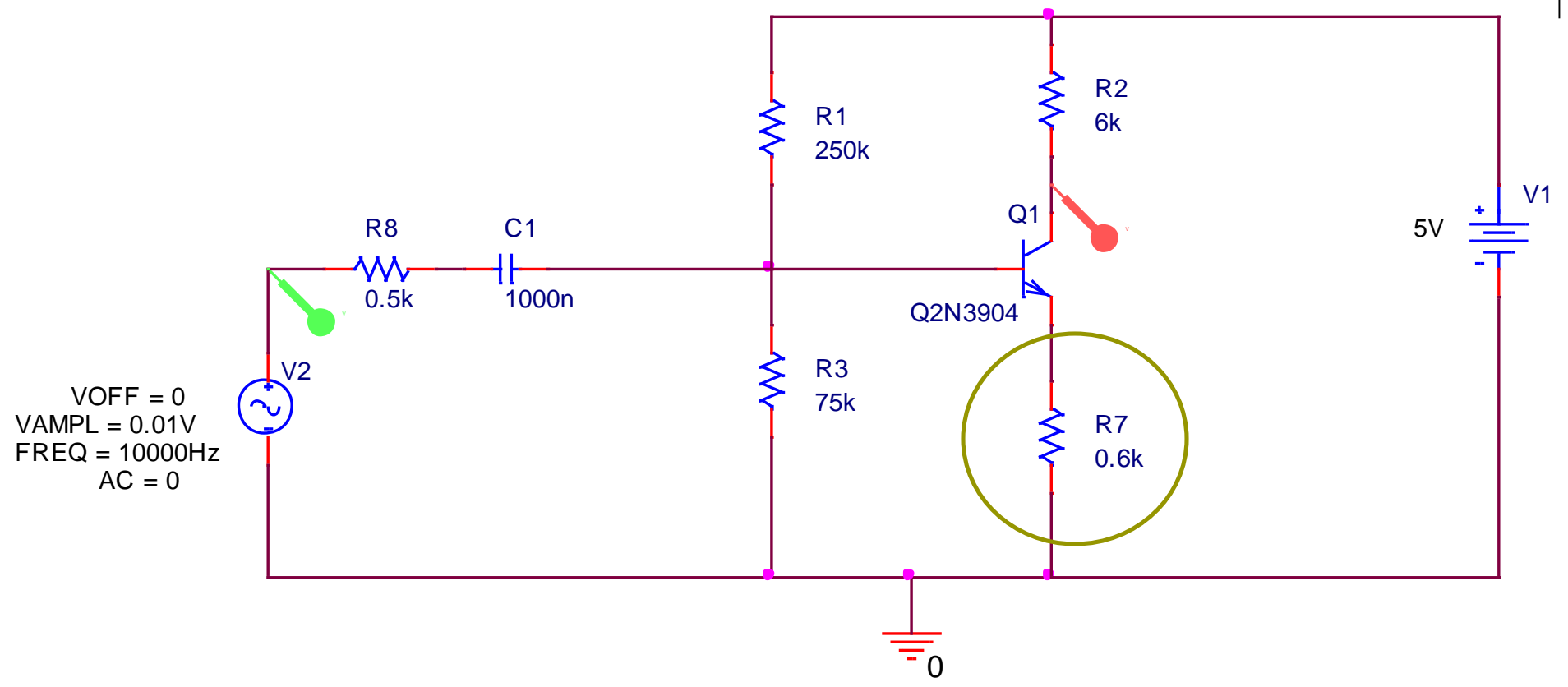
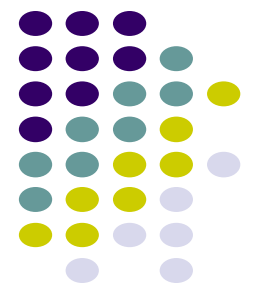
$$R_{TH} = 57.7\text{ k}\Omega$$

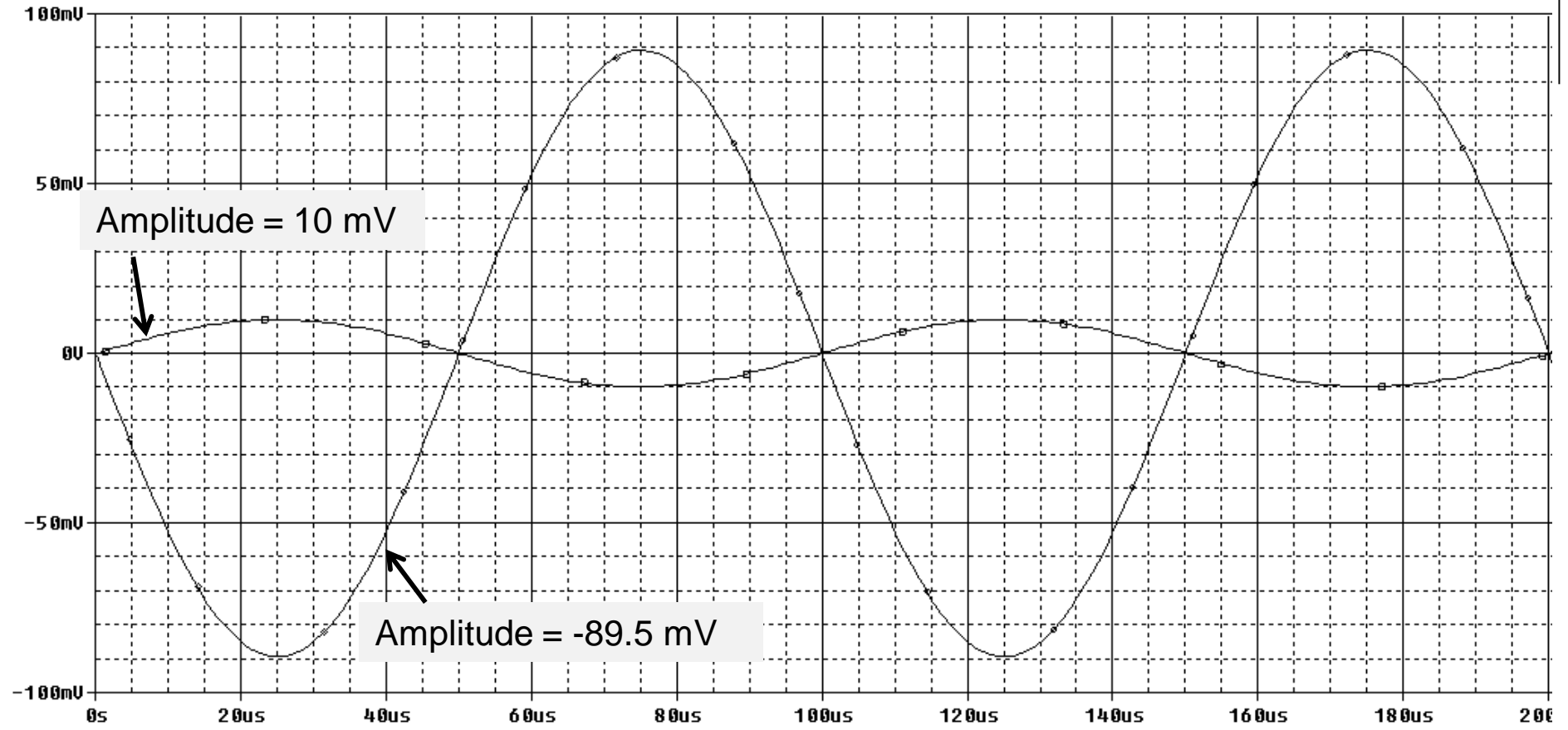
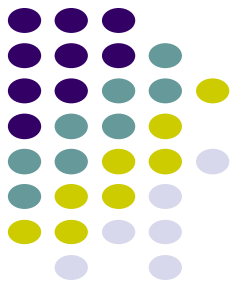
$$V_{TH} = 1.154\text{ V}$$



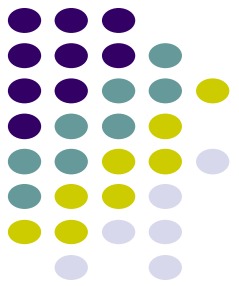


$$v_o/v_s = -8.85$$

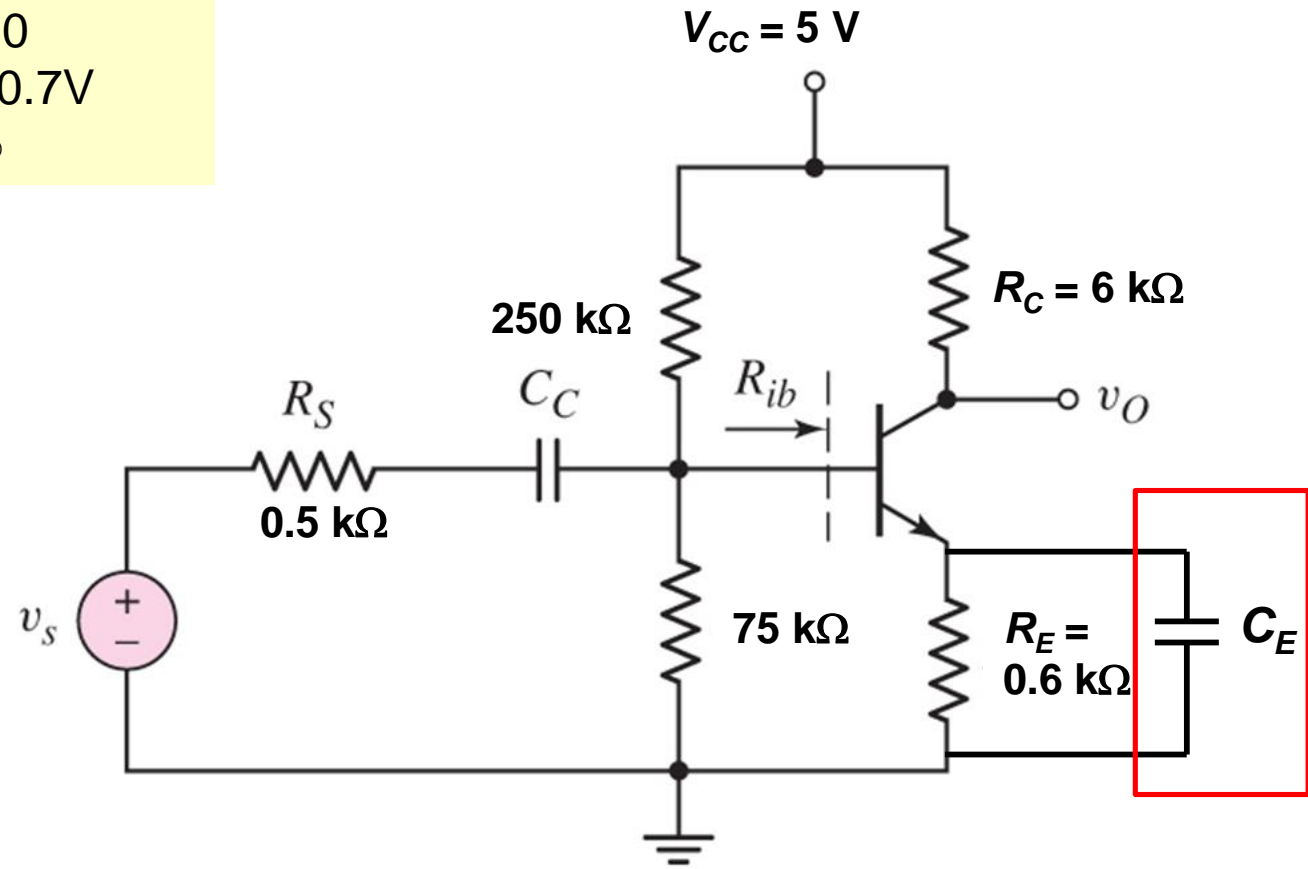


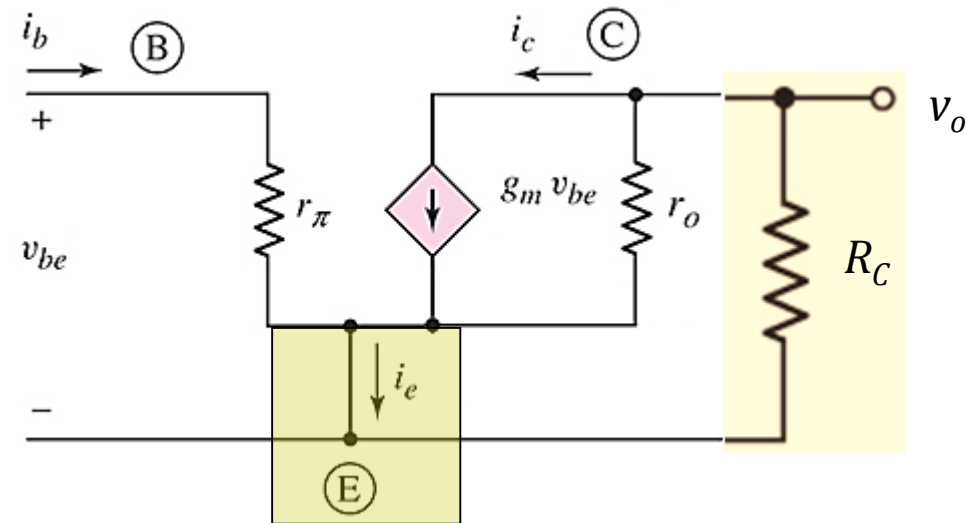
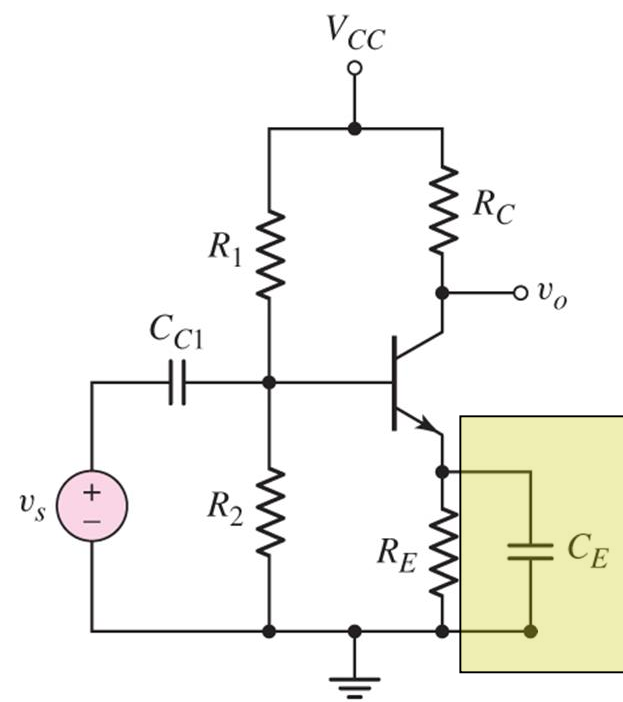
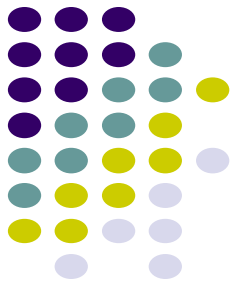


$$\text{Gain} = - (89.5 / 10) = -8.95$$



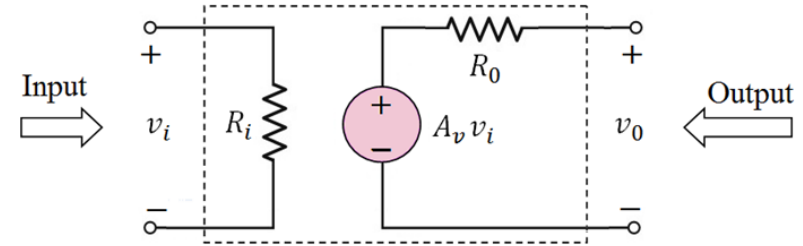
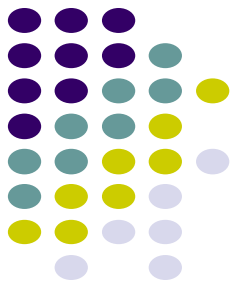
$\beta = 120$
 $V_{BE} = 0.7V$
 $V_A = \infty$





**C_E becomes a short circuit path –
bypass R_E ; hence similar to Type 1**

STEPS TO OBTAIN VOLTAGE AMPLIFIER COMPONENTS – SAME WITH EMITTER GROUNDED



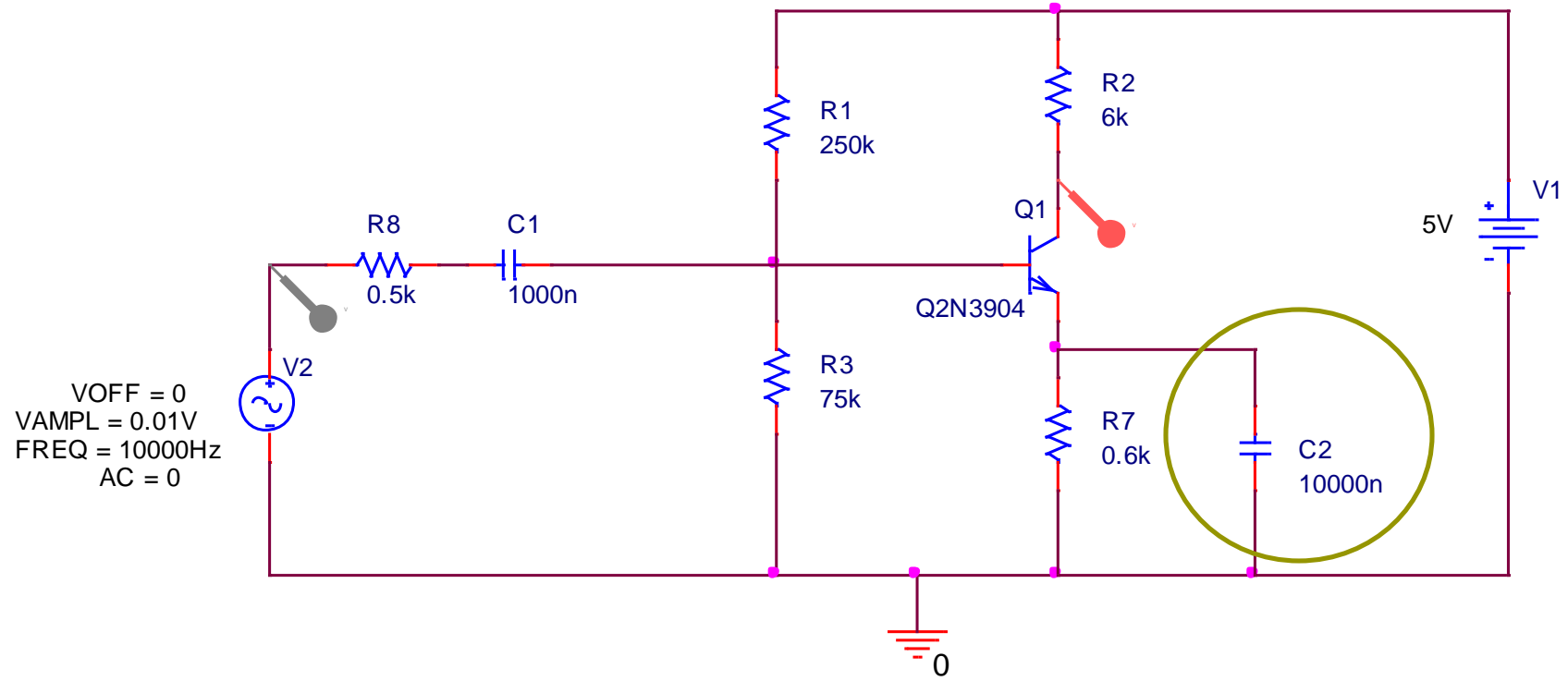
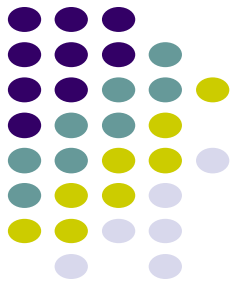
Equivalent circuit of a voltage amplifier

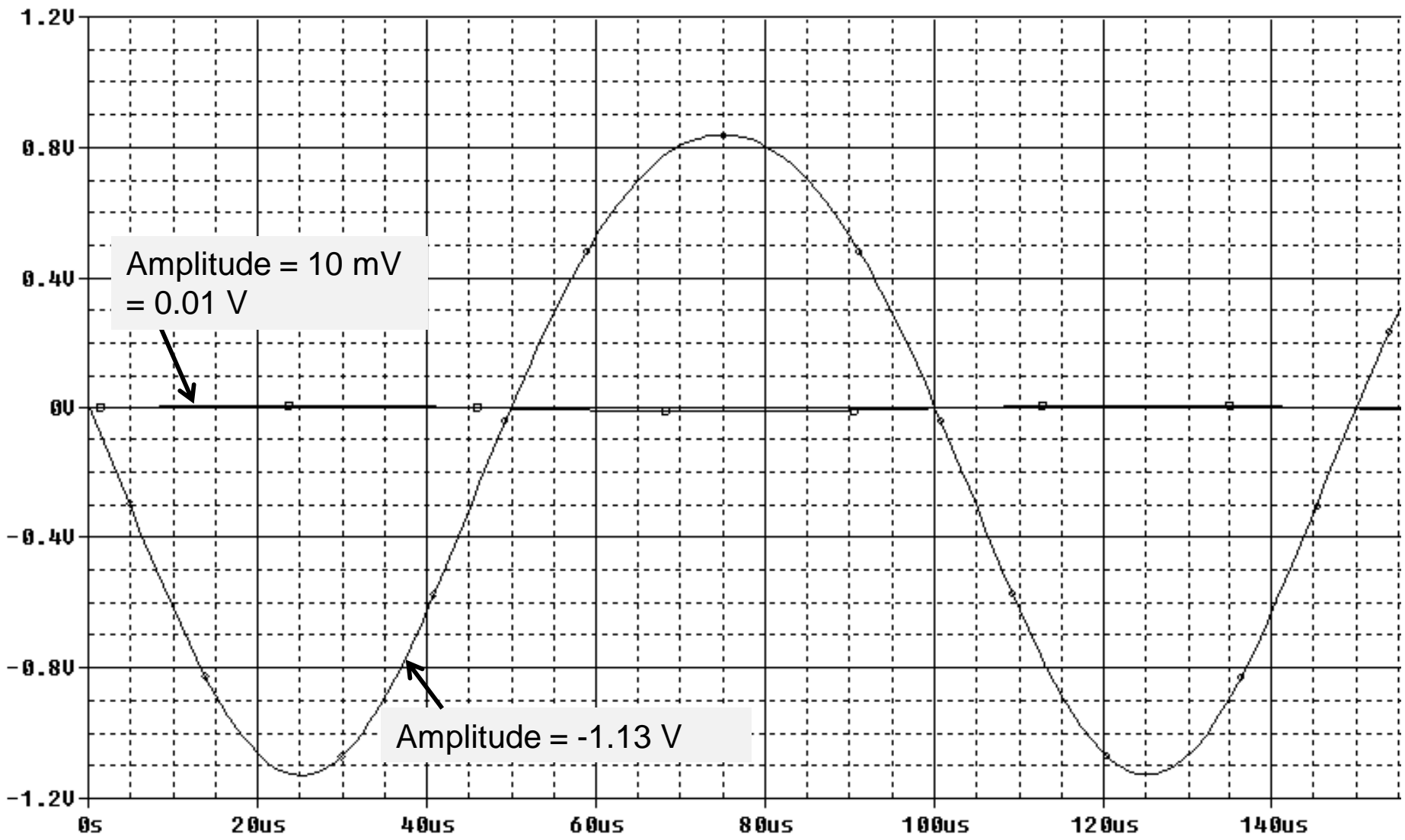
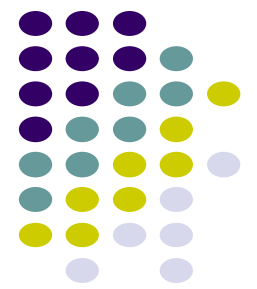
OUTPUT SIDE

1. Get the equivalent resistance at the output side, R_o
2. Get the v_o equation where $v_o = -g_m v_{be} R_o$

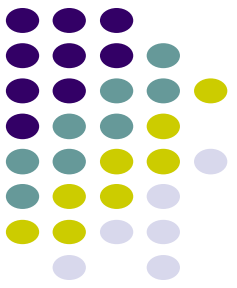
INPUT SIDE

3. Calculate R_i
4. Get v_{be} in terms of v_i
5. Get the open circuit voltage gain

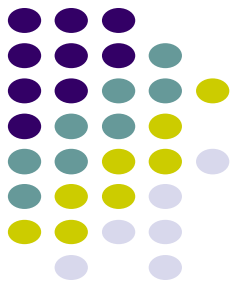




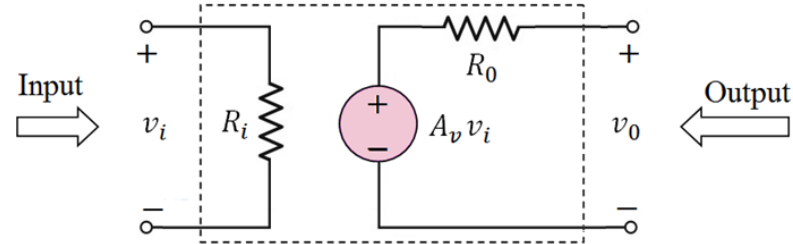
$$\text{Gain} = - (1.13 / 0.01) = - 113$$



- Hence, from the calculation and the simulation, you can observe the improvement in terms of the voltage gain
 - Based on the simulation:
 - From -8.95 (with R_E) to -113 (with bypass capacitor C_E)

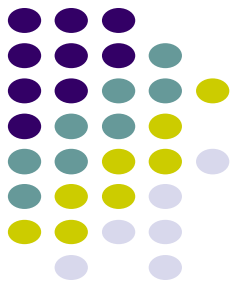
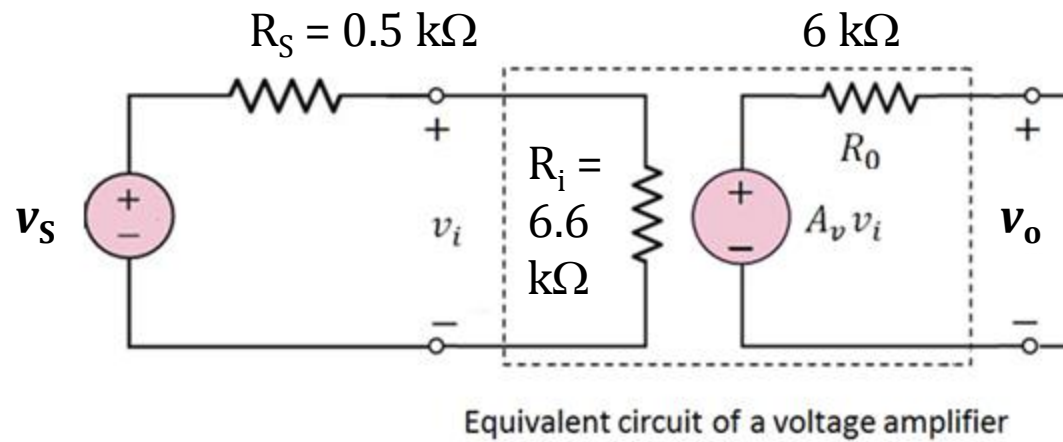


1. $R_o = R_C = 6 \text{ k}\Omega$
2. Equation of v_o : $v_o = - (R_C) g_m v_{be} = - 96.36 v_{be}$
3. Calculate $R_i = R_{TH} || r_{\pi} = 57.7 || 7.46 = 6.6 \text{ k}\Omega$
4. $v_{be} = v_i$



Equivalent circuit of a voltage amplifier

5. $A_v v_i = v_o = - (R_C) g_m v_{be} \leftarrow$ open circuit voltage
 $A_v v_i = - (R_C) g_m v_{be}$ because $v_i = v_{be}$
 $A_v = - (R_C) g_m = - 96.36 \leftarrow$ open circuit voltage gain



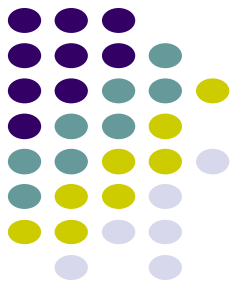
To find **new voltage gain**, v_o/v_s with input signal voltage source, v_s

6. v_i in terms of $v_s \rightarrow$ use voltage divider:
 $v_i = [R_i / (R_i + R_s)] * v_s = [6.6 / 7.1] v_s = 0.9296 v_s$

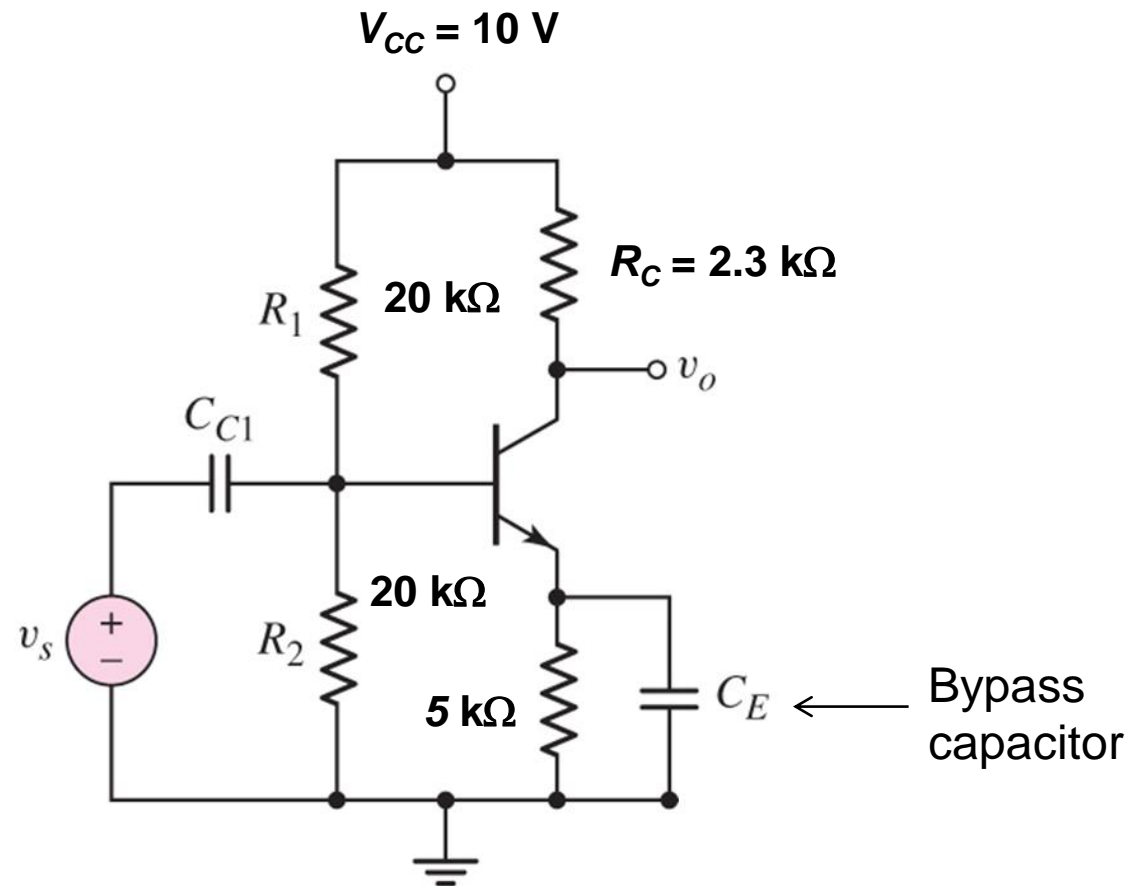
7. $v_o = A_v v_i \leftarrow$ because there is no load resistor
 $v_o = -96.36 (0.9296 v_s)$

$v_o/v_s = - 89.6$

EXAMPLE TYPE 3



$$\begin{aligned}\beta &= 125 \\ V_{BE} &= 0.7\text{V} \\ V_A &= \infty\end{aligned}$$



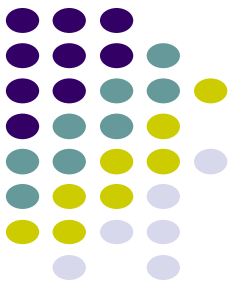
Voltage Divider biasing:

Change to Thevenin Equivalent

$$R_{TH} = 10\text{ k}\Omega$$

$$V_{TH} = 5\text{ V}$$

$$\beta = 125$$
$$V_{BE} = 0.7V$$
$$V_A = \infty$$



- Perform DC analysis to obtain the value of I_C

$$\text{BE loop: } 10 I_B + 0.7 + 5 I_E - 5 = 0$$

$$I_E = 126 I_B$$

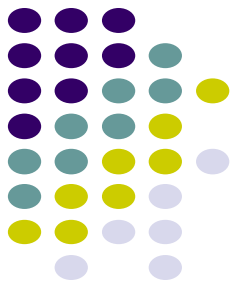
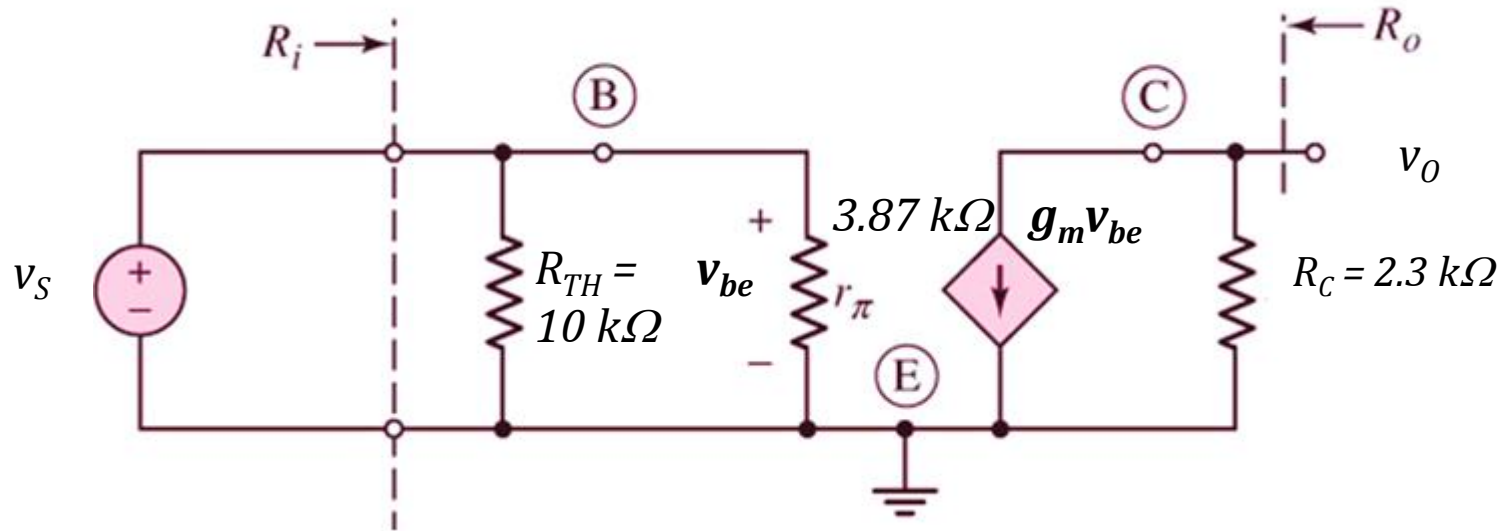
$$10 I_B + 0.7 + 5 (126 I_B) - 5 = 0$$

$$I_B = 4.3 / (10 + 630) = 0.00672\text{mA}$$

$$I_C = \beta I_B = 0.84 \text{ mA}$$

- Calculate the small-signal parameters

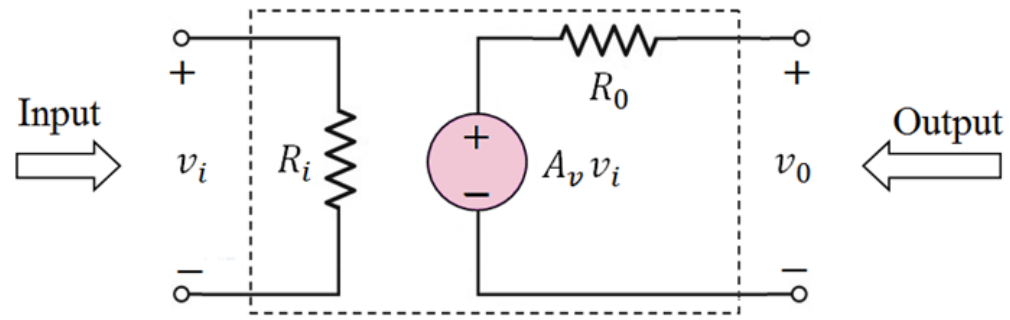
$$r_\pi = 3.87 \text{ k}\Omega, r_o = \infty \text{ k}\Omega \text{ and } g_m = 32.3 \text{ mA/V}$$



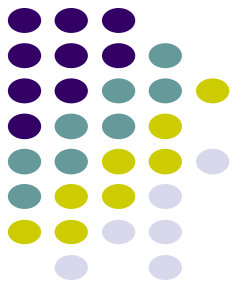
Follow the steps

1. $R_o = R_C = 2.3 \text{ k}\Omega$
2. Equation of v_o : $v_o = - (R_C) g_m v_{be} = - 74.29 v_{be}$

3. Calculate $R_i \rightarrow R_{TH} || r_{\pi} = 2.79 \text{ k}\Omega$
4. $v_{be} = v_i$



Equivalent circuit of a voltage amplifier



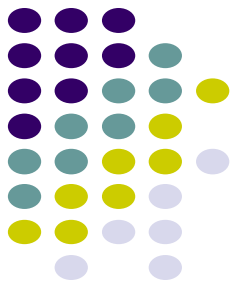
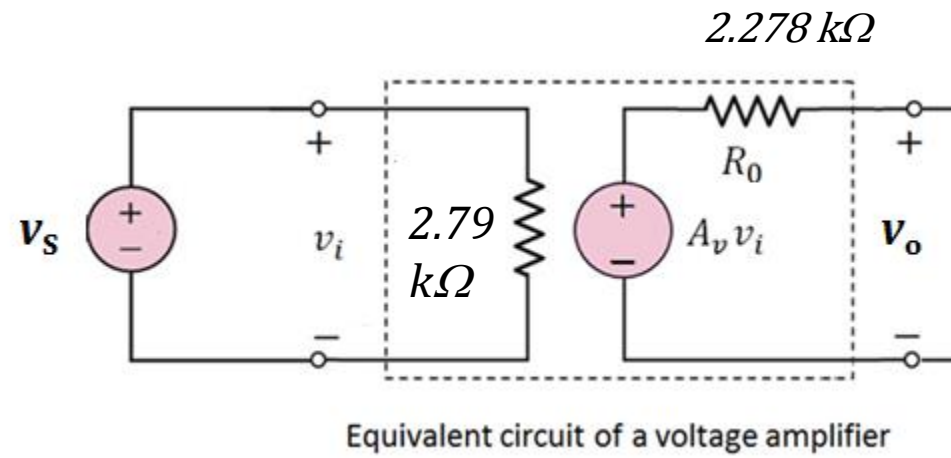
Equation of v_o : $v_o = -74.29 v_{be}$

$$v_{be} = v_i$$

5. $A_v v_i = v_o \leftarrow$ open circuit voltage

$$A_v v_i = -74.29 v_{be} = -74.29 v_i$$

$A_v = -74.29 \leftarrow$ open circuit voltage gain



To find new voltage gain, v_o/v_s with input signal voltage source, v_s

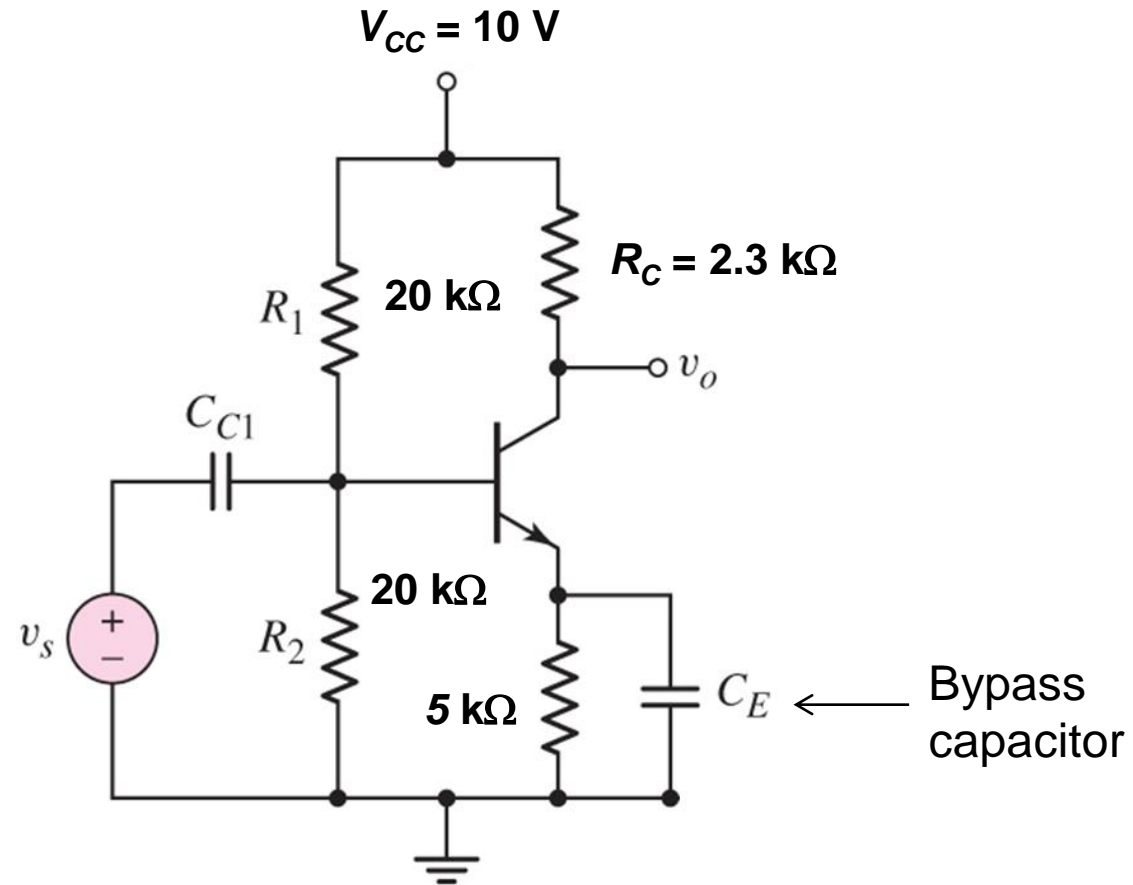
6. $v_i = v_s \rightarrow$ in parallel

7. $v_o = A_v v_i$
 $v_o = -74.29 (v_s)$

$v_o/v_s = -74.29$

EXAMPLE TYPE 3 – with r_o

$$\beta = 125$$
$$V_{BE} = 0.7\text{V}$$
$$V_A = 200\text{V}$$

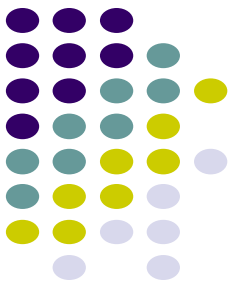


Voltage Divider biasing:

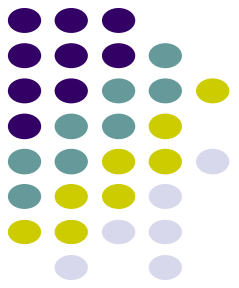
Change to Thevenin Equivalent

$$R_{TH} = 10\text{ k}\Omega$$

$$V_{TH} = 5\text{ V}$$



$$\beta = 125$$
$$V_{BE} = 0.7V$$
$$V_A = 200 V$$



- Perform DC analysis to obtain the value of I_C

$$\text{BE loop: } 10 I_B + 0.7 + 5 I_E - 5 = 0$$

$$I_E = 126 I_B$$

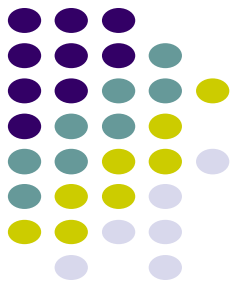
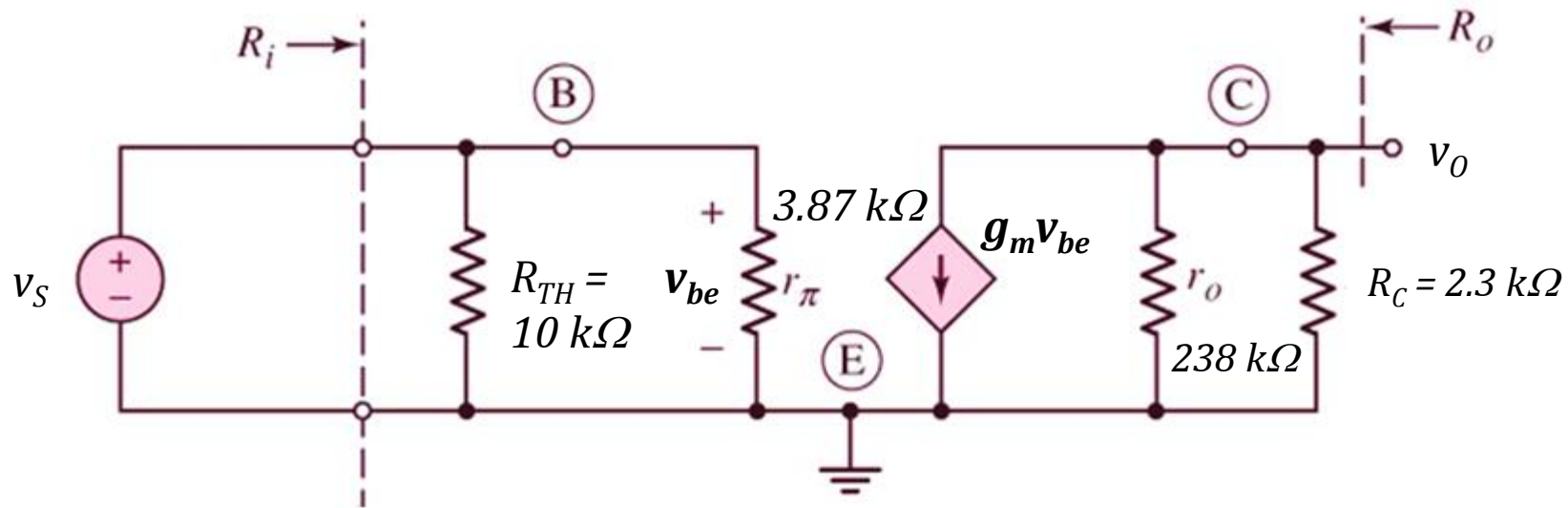
$$10 I_B + 0.7 + 5 (126 I_B) - 5 = 0$$

$$I_B = 4.3 / (10 + 630) = 0.00672\text{mA}$$

$$I_C = \beta I_B = 0.84 \text{ mA}$$

- Calculate the small-signal parameters

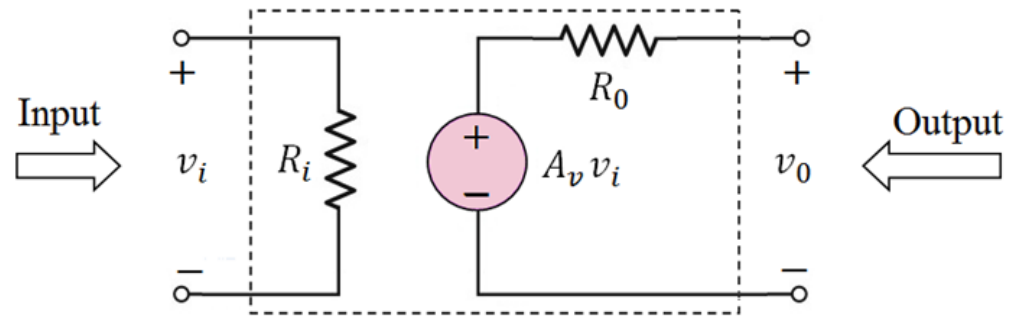
$$r_\pi = 3.87 \text{ k}\Omega, r_o = 238 \text{ k}\Omega \text{ and } g_m = 32.3 \text{ mA/V}$$



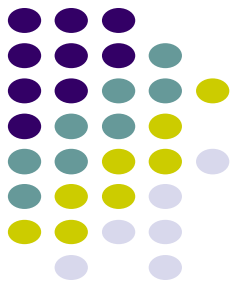
Follow the steps

1. $R_{out} = r_o \parallel R_C = 2.278 \text{ k}\Omega$
2. Equation of v_o : $v_o = - (r_o \parallel R_C) g_m v_{be} = -73.58 v_{be}$

3. Calculate $R_i \rightarrow R_{TH} \parallel r_{\pi} = 2.79 \text{ k}\Omega$
4. $v_{be} = v_i$



Equivalent circuit of a voltage amplifier



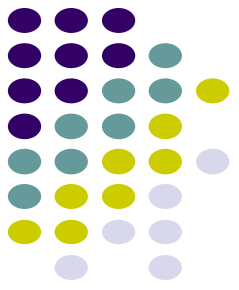
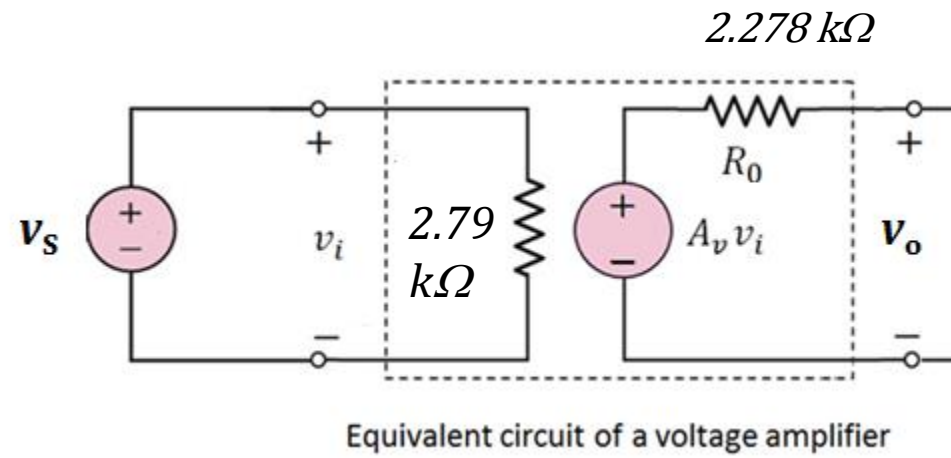
Equation of v_o : $v_o = - (r_o \parallel R_C) g_m v_{be} = -73.58 v_{be}$

$v_{be} = v_i$

5. $A_v v_i = v_o \leftarrow$ open circuit voltage

$A_v v_i = -73.58 v_{be} = -73.58 v_i$

$A_v = -73.58 \leftarrow$ open circuit voltage gain



To find new voltage gain, v_o/v_s with input signal voltage source, v_s

6. $v_i = v_s \rightarrow$ in parallel

7. $v_o = A_v v_i$
 $v_o = -73.58 (v_s)$

$v_o/v_s = -73.58$